

XA-9596  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Kiyoshi NAKAI et al.

Appln. No.: 10/041,601

Group Art Unit: 2818

Filed: January 10, 2002

Examiner: T. Nguyen

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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PETITION FOR EXTENSION OF TIME

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

It is respectfully requested that the time now set for response be extended, retroactively, by two months. If any further extension of time is required to avoid abandonment of this application, such extension is hereby requested. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 that may

be required by this paper to Deposit Account No. 50-1165.

Respectfully submitted,

NHS:lmb:jab

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February 2, 2004

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CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on February 2, 2004.

Nelson H. Shapiro  
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